



BSP for Microsoft Windows* 7 (WIN7, WES7 & POSReady 7) 32-bit & 64-bit for Intel® Pentium™ Processor N3700 and Intel® Celeron™ Processor N3150, N3050 and N3000 Product Family

Release Notes

July 2015

*Gold
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Contents

1	Introduction	5
1.1	Scope of document	5
1.2	System Requirements	5
1.3	Acronyms and Terminology	5
2	Release Summary	6
2.1	Release Details	7
2.2	Release Contents	7
2.3	Best Known Configurations	8
2.4	The Ready Feature	9
3	Release Notes	11
3.1	GPIO Driver	11
3.2	I ² C Driver	12
3.3	HS-UART Driver	13
3.4	LPSS DMA Driver	14
3.5	Errata, Closed Issues, Known Issues	15
	3.5.1 Errata	15
	3.5.2 Closed Issues	15
	3.5.3 Known Issues	16



Revision History

Revision Number	Description	Revision Date
0.5	Initial release for Beta	April 2015
1.0	Gold release	July 2015



1 Introduction

1.1 Scope of document

This document consists of Release Notes about Intel developed GPIO, I²C, and HS-UART drivers for Windows* 7, Windows Embedded Standard 7 and Windows Embedded POSReady 7. This document also includes information of Window* 7 Inbox drivers that have been validated on Intel® Pentium™ Processor N3700 and Intel® Celeron™ Processor N3150, N3050 and N3000 Product Family.

In this Release Notes, the driver interfaces, limitations, errata, closed issues, known issues; platform and driver software best known methods are covered.

This document is intended for OEMs and ODMs that are enabling Win7 and WES7 drivers with Intel® Pentium™ Processor N3700 and Intel® Celeron™ Processor N3150, N3050 and N3000 Product Family.

1.2 System Requirements

The following Operating Systems are supported:

- Windows* 7 Operating System (32-bit and 64-bit versions)
- Windows* Embedded Standard 7 Operating System (32-bit and 64-bit versions)
- Windows* Embedded POSReady 7 Operating System (32-bit and 64-bit versions)

1.3 Acronyms and Terminology

Term	Description
API	Application Programming Interface
ATAPI	ATA Packet Interface
BSP	Board Support Package
CRB	Customer Reference Board
DMA	Direct Memory Access
eMMC	Embedded Multimedia Card
GPIO	General Purpose Input/Output



HSUART	High Speed Universal Asynchronous Receiver/Transmitter
I2C	Inter-Integrated Circuit
IO	Input Output
IOCTL	Input Output Control
KITL	Kernel Independent Transport Layer
LAN	Local Area Network
MSDN	Microsoft Developer Network
OS	Operating System
PCI	Peripheral Component Interconnect
SATA	Serial ATA
SD	Secured Digital
USB	Universal Serial Bus

1.4 Related Documents

Document	Document No. /Location
<i>The I/O Drivers of Microsoft Windows* 7 (WIN*7, WES*7 and POSReady* 7) 32-bit and 64-bit for Intel® Pentium™ Processor N3700 and Intel® Celeron™ Processor N3150, N3050 and N3000 Product Family User Guide</i>	557841
<i>The I/O Drivers of Microsoft Windows* 7 (WIN*7, WES*7 and POSReady* 7) 32-bit and 64-bit for Intel® Pentium™ Processor N3700 and Intel® Celeron™ Processor N3150, N3050 and N3000 Product Family Developers Manual</i>	557898



2 Release Summary

2.1 Release Details

Driver Version: 1.2.3.0616

Released on July, 2015.

2.2 Release Contents

The contents of this release include:

- Intel® Processor Win7 IO Drivers 32Bit and 64Bit Driver Installer
Both "Intel Processor Win7 IO Drivers 32Bit.msi" and "Intel Processor Win7 IO Drivers 64Bit.msi" installer will install the following drivers on your system:
 - Intel® Celeron®/Pentium® Processor UART Host Controller
 - Intel® Celeron®/Pentium® Processor I²C Controller
 - Intel® Celeron®/Pentium® Processor GPIO Controller
 - Intel® Celeron®/Pentium® Processor Low Power Subsystem DMA Device
- Intel® Processor Win7 IO Drivers – Software Developer Guide
 - Headers Files for GPIO and I²C
 - Software Developers Manual for Windows 7 IO Drivers
- Intel® Processor Win7 IO Drivers Release Notes & User's Guide
- Intel® Software License Agreement



2.3 Best Known Configurations

Hardware Configuration		
Category	Description	Rev/Type/Source
CRB	Cherryhill	REV D FAB 204
SOC	Intel® Pentium™ Processor N3700 and Intel® Celeron™ Processor N3150, N3050 and N3000 Product Family.	B1 : QHAW
Display	HDMI	
Memory	Cherryhills: 4 GB DDR3 (2x2GB)	
Firmware Configuration		
CRB BIOS	BSW_SPI_Quad_R10_Production_BRASWEL_X64_R_X060_00_ME-2.0.0.2048 (vBIOS 1004)	Refer to BIOS release
KSC	N/A	N/A
Driver/OS Configuration		
Operating System	Windows* 7 SP1 Windows Embedded Standard 7 SP1 Windows Embedded POSReady 7 SP1	MSDN
Graphics Driver	38.15.0.1075/39.15.0.1075	EMGD
GPIO Driver	1.2.2.1008	Intel
I ² C Driver	1.2.2.1008	Intel
SPI Driver	N/A	N/A
HS-UART Driver	1.2.2.1010	Intel
SD and eMMC Driver	1.2.3.1010	Intel
Chipset INF	10.1.1.7	Intel
USB 3.0 Driver	4.0.0.36 (32bit and 64bit)	Intel



2.4 The Ready Feature

Area	Feature	Source	Ready*
SIO	General SIO feature	Win7 Inbox driver	Yes
USB	General USB 2.0 feature	Win7 Inbox driver	N/A
	General USB 3.0 feature	Intel USB 3.0	Yes
	USB3.0 Boot	Win7 Inbox driver	Yes
SATA	General SATA feature	Win7 Inbox driver	Yes
	General SATA2 feature	Win7 Inbox driver	N/A
	General SATA3 feature	Win7 Inbox driver	Yes
PCIe	General PCIe feature	Win7 Inbox driver	Yes
Intel® Graphics Driver	General graphics feature	Intel	Yes
High Definition Audio	General HD Audio feature	Win7 Inbox driver	Yes
	HDMI Audio	Integrated in EMGD driver	Yes
Power Management	Power Mgmt S0 and S5	N/A	Yes
	Power Mgmt Sleep S3	Intel	Yes
	Power Mgmt Hibernate S4	Intel	Yes
GPIO Driver	Direction Setting	Intel	Yes
	Multiplexing Setting		Yes
	Level Value Setting		Yes
	Pin Setting Query		Yes



I²C Driver	Standard Mode (100Kbps)	Intel	Yes
	Fast Mode (400Kbps)		Yes
HS-UART Driver	Baud rate support up to 4000000	Intel	Yes
	Data size 5, 6, 7, 8-bit		Yes
	Odd, even, none parity		Yes
	1, 1.5, and 2 stop bits		Yes
	Hardware & No flow control & Software flow control		Yes
DMA Feature (I²C, HS-UART)	DMA support for I ² C, and HS-UART	Intel	Yes
eMMC Driver	Version 4.5 storage	Intel	Yes
SD2 Driver	SD and SDHC cards	Intel	Yes
	Class 2, 4, 6, and 10		Yes
	1-bit and 4-bit bus mode		Yes
	FAT32, exFAT filesystem		Yes
	ADMA transfer mode		Yes

Note: Refer to the next section for the limitations of the GPIO/I²C/HS-UART/DMA feature.



3 Release Notes

3.1 GPIO Driver

GPIO Driver interface is exposed by a series of IOCTLS. A separated C header file provides the definition of the IOCTLS and a separated programming guide provides how to program with the IOCTLS.

Driver Binary Package:

- `iaiogpio.inf`
- `iaiogpio.sys`
- `iaiogpio.cat`

Driver Interface Header: `GPIOPublic.h`

Enabled Features:

- Support GPIO multiplexing setting.
- Support GPIO setting query, query multiplexing information of GPIO pin.
- Support GPIO direction setting, configure selected GPIO pin as input or output pin.
- Support GPIO read pin, read pin's level value when GPIO pin is configured as input pin.
- Support GPIO write pin, configure pin level to high or low when it is configured as output pin.

Limitations:

- No known limitation



3.2 I²C Driver

I²C Driver interface is exposed by a series of IOCTLs. A separated C header file provides the definition of the IOCTLs and a separated programming guide provides how to program with the IOCTLs.

There are total seven I²C controllers on Intel[®] Atom™ E3000 Processor, Intel[®] Celeron[®] Processor N2XXX and Intel[®] Celeron[®] Processor J1XXX which share the same DMA engine. Hence, transferring a big data size will cause one I²C controller to occupy DMA engine for a long duration.

Application can use multiple single transfers or IOCTL_I2C_EXECUTE_SEQUENCE interface to transfer big data.

By default, I²C driver uses DMA to copy data between peripheral and system memory, but can set windows registry to disable DMA feature and copy data by PIO mode. Refer to BKM section to about how set the registry.

Driver Binary Package:

- iaioi2c.inf
- iaioi2c.sys
- iaioi2c.cat

Driver Interface Header: I2CPublic.h

Enabled Features:

- Support 7-bit address Mode
- Support Standard Mode (100Kbps)
- Support Fast Mode (400Kbps)
- Support polling of IO data transfer

Limitations:

- The maximum single transfer size is limited to 64Kbytes. Multiple transfer is required for data size of more than 64KB.



3.3 HS-UART Driver

HS-UART Driver interface is exposed by standard Windows Serial Communication interface. Refer to Serial Communications in Win32 in MSDN to understand the details.

<http://msdn.microsoft.com/en-us/library/ms810467.aspx>

Following APIs of serial communication in Win32 are supported in MR1 driver release:

- [SetCommMask](#)
- [WaitCommEvent](#)
- [GetCommMask](#)

Remark: These serial series masks: "SERIAL_EV_PERR, SERIAL_EV_RX80FULL, SERIAL_EV_EVENT1, SERIAL_EV_EVENT2" used in above three functions are not supported. Others are supported.

Intel has no plan to support following APIs of serial communication in Win32:

- [SetupComm](#)
- [SetCommBreak](#)
- [ClearCommBreak](#)
- [EscapeCommFunction](#) (no support for parameter set to SETBREAK and CLRBREAK)

Driver Binary Package:

- iaioart.inf
- iaioart.sys
- iaioart.cat

Driver Interface Header: See MSDN as above link.

Enabled Features:

- Support baud rates: 300 – 921600, up to 3686400 by default as specified in the "Bay Trail-I SoC External Design Specification" document, Section 27.2.3 Baud Rate Generator. For setting baud rates of 1M, 2M, 3M, and 4M, see BKM section below.
- Support data size of 5,6,7, and 8-bit
- Support none, odd and even parity
- Support 1, 1.5, and 2 stop bits
- Support "Hardware" and "No" flow control and software flow control
- Supports Serial Device Control Requests (IOCTLs) defined by MSFT for serial controllers in Windows. See Limitations below for the IOCTLs that will be enabled in Gold release.



Limitations:

- HS-UART driver doesn't support DMA transfer with software flow control. When application uses the software flow control, the HS-UART will use PIO mode to copy data between peripheral and system memory.
- Software flow control only support maximum baud rate up to 115200. Recommended to use hardware flow control for data transfer for high baud rate.
- When 1.5 stop bits is used, the data size can only be supported up to 5 bits.
- IOCTLs are not supported in driver:

IOCTL_SERIAL_XOFF_COUNTER

IOCTL_SERIAL_LSRMST_INSERT

IOCTL_SERIAL_SET_BREAK_ON

IOCTL_SERIAL_SET_BREAK_OFF

3.4 LPSS DMA Driver

LPSS DMA Driver is not exposed publicly and only I²C, HS-UART driver are able to access the DMA driver interface.



3.5 Errata, Closed Issues, Known Issues

3.5.1 Errata

Issue #	Description	Impact	Recommendation
4634937	HS-UART COM number increases every time after uninstall/reinstall of UART driver	For those applications using COM ports of HSUART, user need to enable changing input parameter of COM number	Change the HS-UART COM ports in the application whenever the UART driver is reinstalled.
4635034	System unable to load into Windows after wake up from hibernate by hitting USBkeyboard and mouse when XHCI mode in BIOS is set to 'Auto' or 'Smart Auto'	User failed to resume the system back from hibernate when XHCI mode is set to "Auto" or smart Auto	Change XHCI mode to "Enable".

3.5.2 Closed Issues

Issue #	Description
5221317	[CHV][WIN7]USB3.0 pendrive unable to be detected intermittently after SUT 1st reboot or shut down.
5221319	[BSW]Low volume from the back panel green audio jack during audio playback.
5221356	HSUART/UART don't support more than 1Mbps due to transceiver limitation
5221393	[CHH][WIN7] Several devices yellow bang in Device Manager
5221401	[CHH] USB keyboard and mouse not functioning in Windows Advance Boot Option page.



3.5.3 Known Issues

Issue #	Description	Impact	Recommendation
4994734	Super Speed USB 3.0 Pendrive Performance Drop in WES 7	WEC7 USB3.0 Super Speed thumb drive , for example Lexar® JumpDrive® P10 USB 3.0 Flash Drive 32GB (up to 265MB/s read, 245MB/s write)	Use the standard USB3.0 Pen Drive, for example Corsair Voyager (up to 80MB/s read, 40MB/s write).
5221392	[CHH][WIN7] USB3.0 Read/Write not up to expectation.	Very fast USB3.0 device will notice drop in performance	Still can be used and within USB3.0 range
5221464	[CHH][WES7]UART data mismatch when running the test with None and Software flow control	Customer cannot use HS-UART at certain baud rate reliably	Use HW flow control